

IN THE CLAIMS

Please amend the claims as follows:

1-14. (Canceled).

15. (Original) A large scale module comprising:

- (a) a heat sink;
- (b) a metallic frame having a plurality of openings disposed on said heat sink;
- (c) a plurality of semiconductor power modules disposed on said heat sink so as to be mounted in said openings;
- (d) a plurality of sealing members disposed between said respective semiconductor power modules and said metallic frame;
- (e) a plastic cover for covering one surface of said metallic frame on which said semiconductor power module is mounted; and
- (f) a resin filled into said cover.

16. (Original) The large scale module of claim 15, wherein respective electrodes of said semiconductor power module are connected in parallel or in series and connected to outer electrodes disposed on a top surface of said cover.

17. (Original) The large scale module of claim 15, wherein a second surface opposite to a first surface attached to the heat sink is covered by said plastic cover.

18. (Currently Amended) The large scale module of claim 15, wherein said semiconductor power module comprising:

- (a) a ceramic substrate;

- (b) a metallic plate bonded to a surface of said substrate;
- (c) a cylindrical metallic flange which is hermetically bonded to said ~~substrate~~
substrate at an outer circumference of said substrate, separated from said metallic plate;
- (d) a disk-shaped ceramic housing for hermetically sealing an opening of said metallic flange; and
- (e) at least one or more semiconductor chips mounted on and soldered to said metallic plate.

19. (Original) The large scale module of claim 15, wherein said semiconductor power module comprising:

- (a) a ceramic substrate;
- (b) a metallic plate bonded to a surface of said substrate;
- (c) a cylindrical metallic flange which is hermetically bonded to a surface of said metallic plate at a boundary of said metallic plate;
- (d) a disk-shaped ceramic housing for hermetically sealing an opening of said metallic flange; and
- (e) at least one or more semiconductor chips mounted on and soldered to said metallic plate.